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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/903,059 Confirmation No.: 9375
First Named Inventor: Bulucea, Constantin Filing Date: 10 July 2001
Group Art Unit: 2814 Examiner: Farhani, D.
Atty. Docket No.: NS-4971 US
Title: Gate-Enhanced Junction Varactor
Assignee(s): National Semiconductor Corporation

Mountain View, California
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AMENDMENT

Sir:

Responsive to the Office Action mailed 18 November 2004, please amend the above patent application in the following manner.

IN THE CLAIMS

Add new Claims 123 - 130 so that the claims are as follows:

1 - 16. (Canceled)

17. (Previously amended) A structure comprising a varactor which comprises:

a plate region and a body region of a semiconductor body, the body region being of a first conductivity type, the plate region being of a second conductivity type opposite to the first conductivity type, the plate and body regions meeting each other to form a p-n junction;

a plate electrode and a body electrode respectively connected to the plate and body regions, the plate electrode being at a plate-to-body bias voltage relative to the body electrode;

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